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**Complete if Known**

Application Number	10/699,568
Filing Date	October 31, 2003
First Named Inventor	Surya Varanasi
Art Unit	2633
Examiner Name	Not yet assigned
Attorney Docket Number	003.P003
Sheet 1 of 1	

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/FD/	1	LINK TRUNKING AND MEASURING LINK LATENCY IN FIBRE CHANNEL FABRIC, Inventors: David C. Banks, et al., Application No: 10/135,615, Filed: 4/30/02, 39 Pgs.	
/FD/	2	LOAD BALANCING IN A NETWORK COMPRISING COMMUNICATION PATHS HAVING DIFFERENT BANDWIDTHS, Application No: 10/208,969, Filed: 7/31/02, 24 Pgs.	
/FD/	3	DYNAMIC PATH SELECTION WITH IN-ORDER DELIVERY WITHIN SEQUENCE IN A COMMUNICATION NETWORK, Application No: 10/059,760, Filed: 1/29/02, 46 Pgs.	

Examiner Signature	/Frank Duong/	Date Considered	07/07/2007
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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